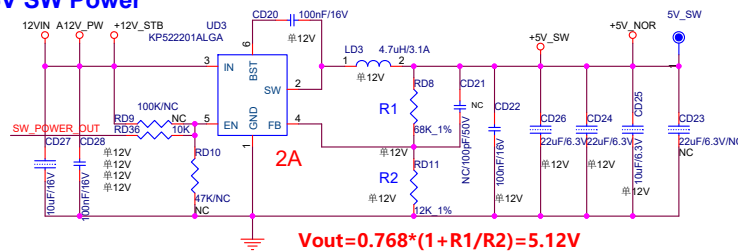
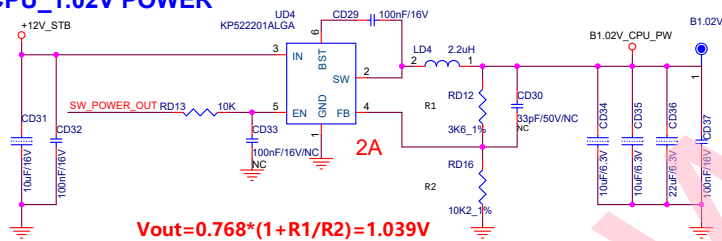


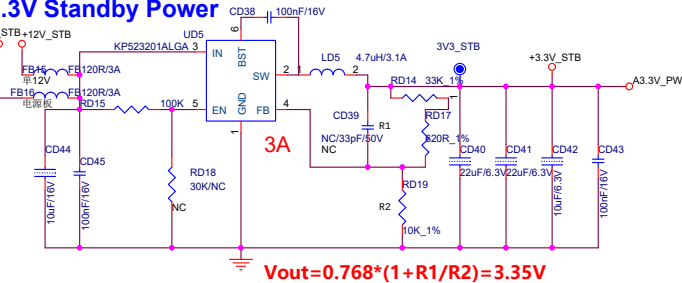
5V SW Power



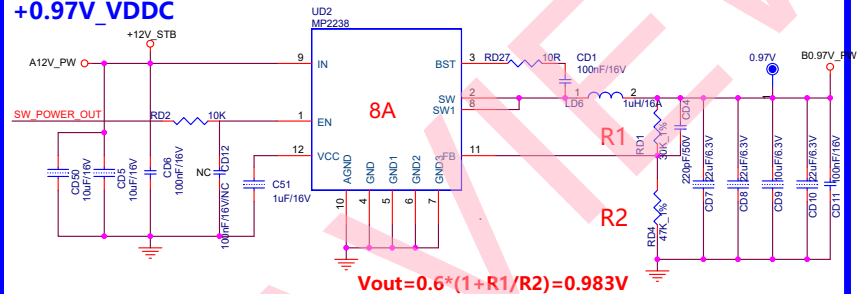
CPU_1.02V POWER



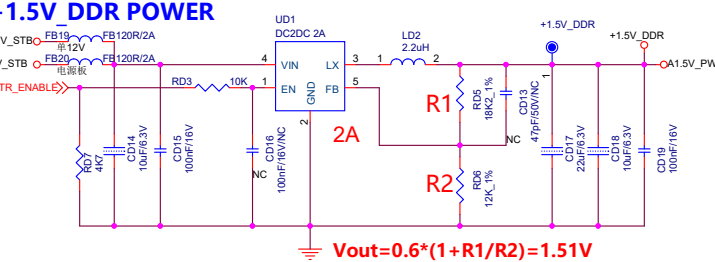
3.3V Standby Power



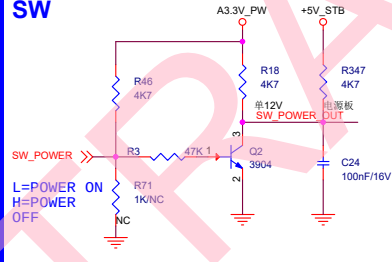
+0.97V_VDDC



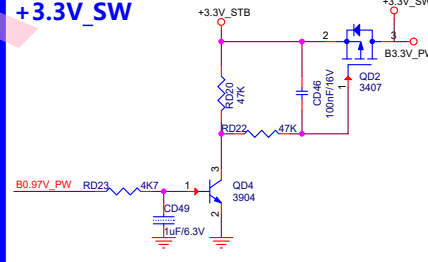
+1.5V_DDR POWER



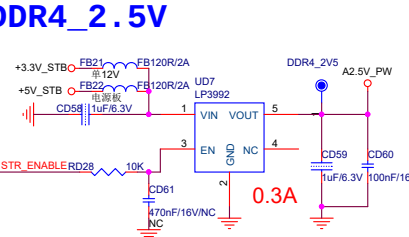
SW



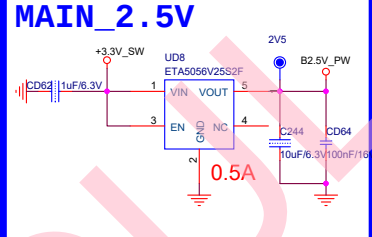
+3.3V_SW



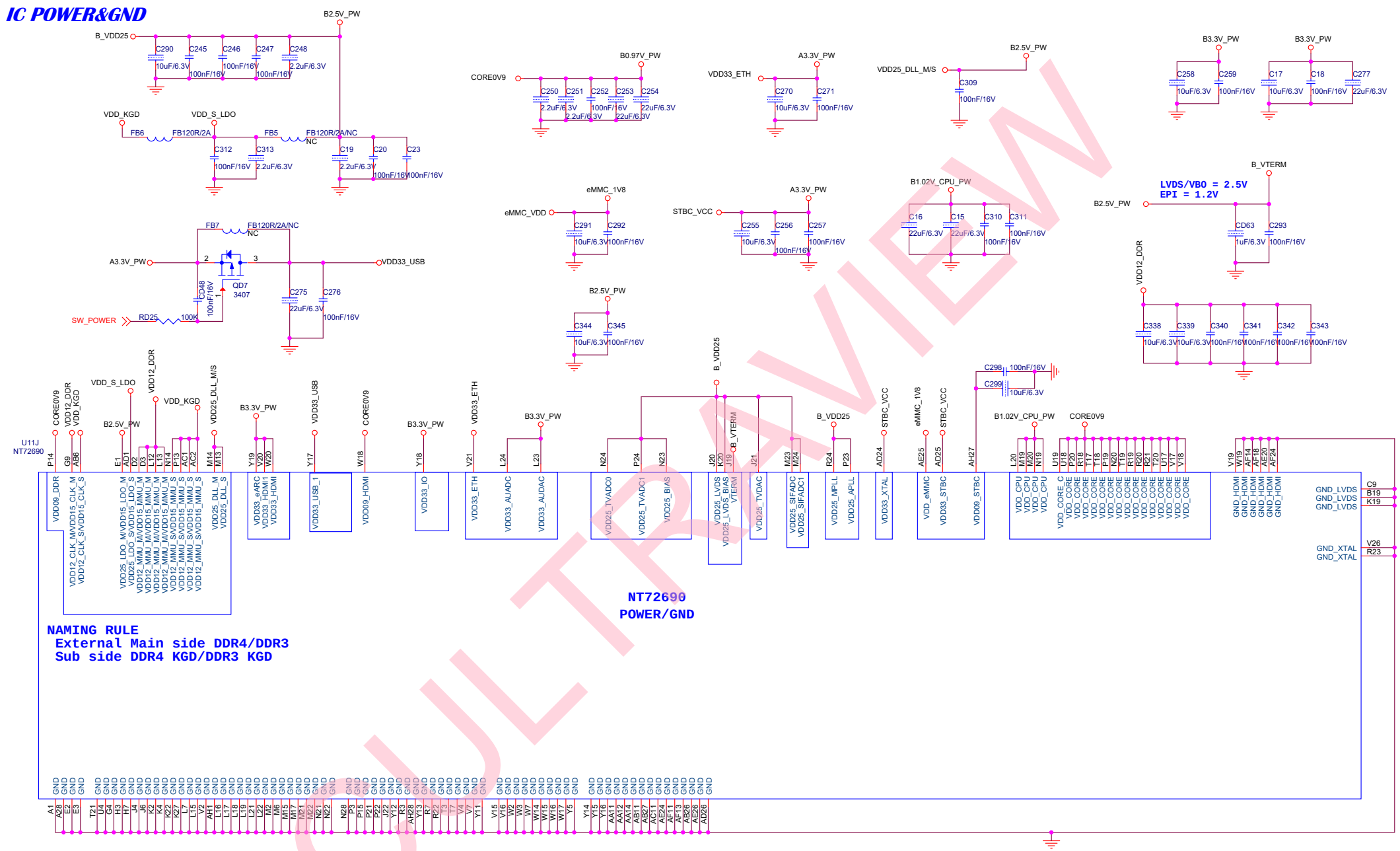
DDR4_2.5V

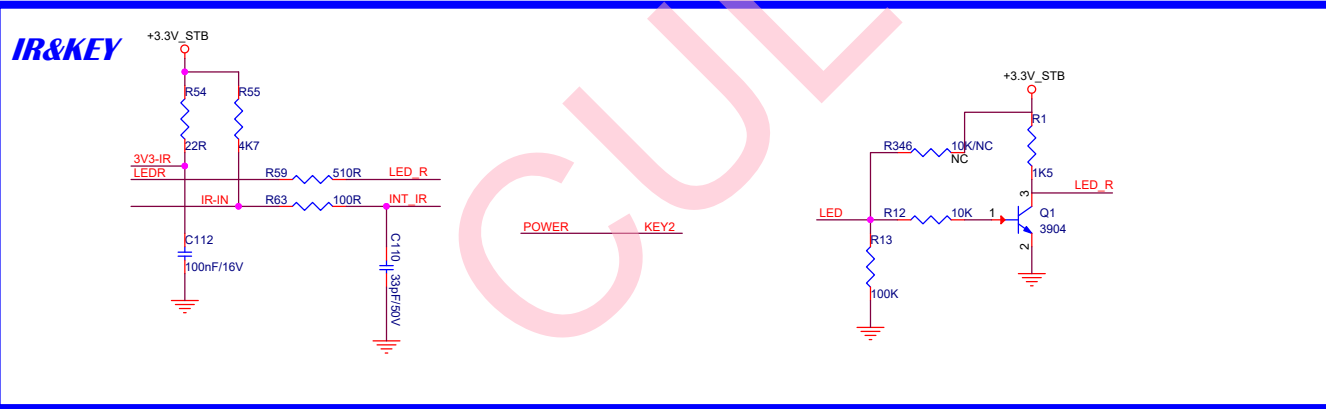


MAIN_2.5V

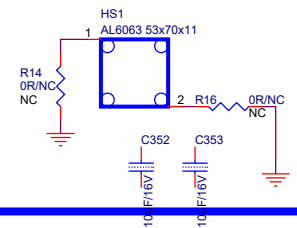
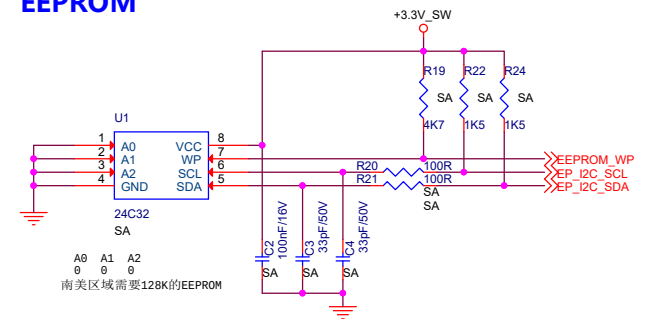


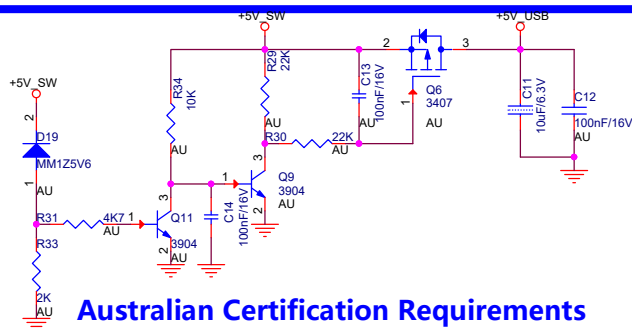
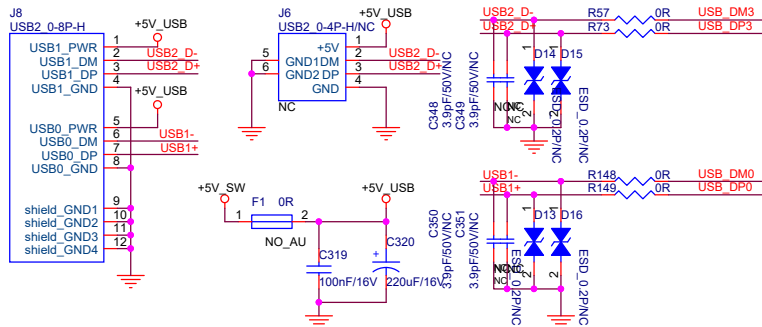
IC POWER&GND





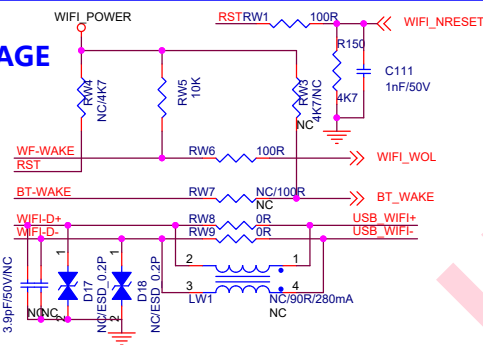
Heat Sink



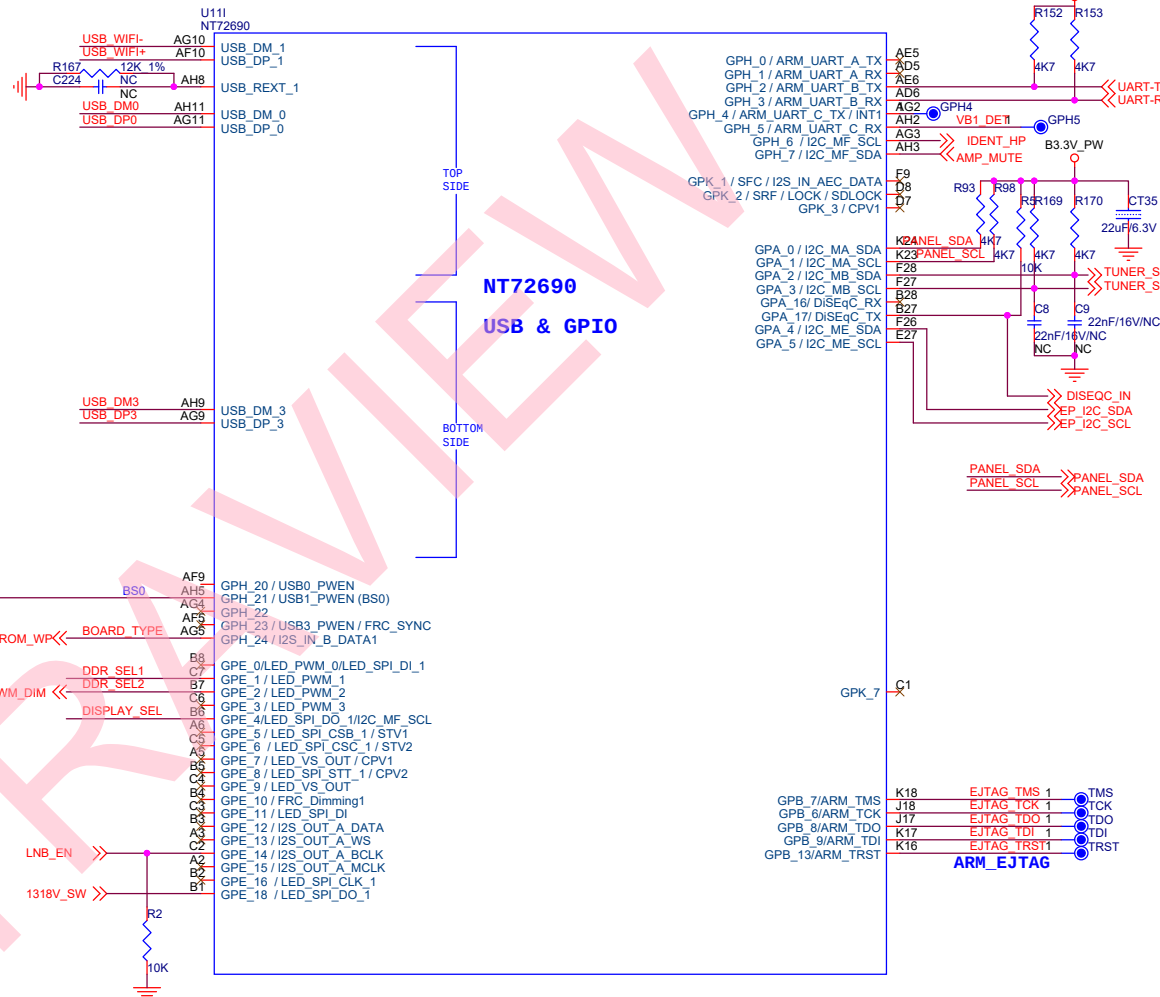
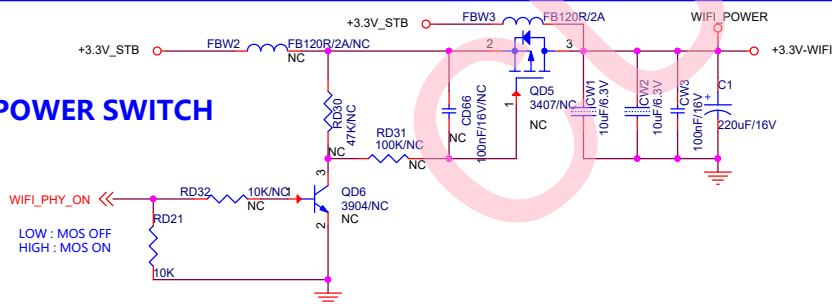


Australian Certification Requirements

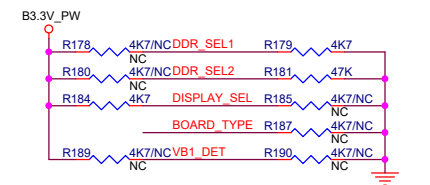
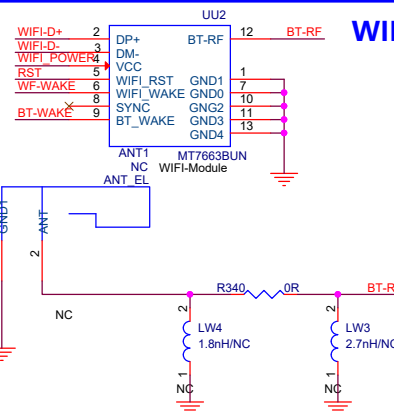
SIGNAL OFFPAGE



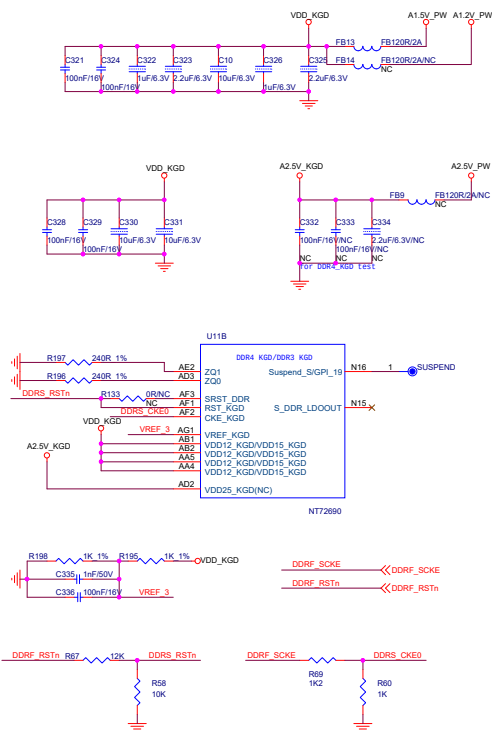
POWER SWITCH



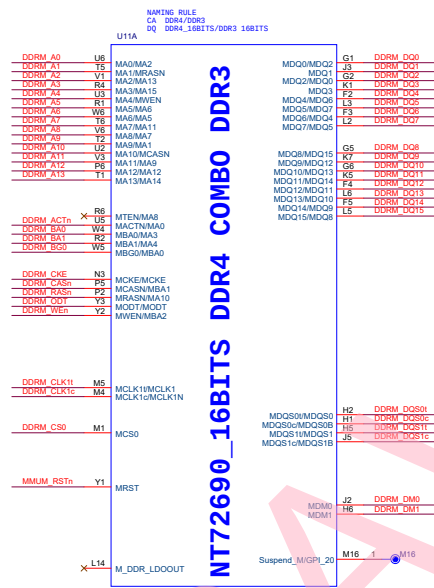
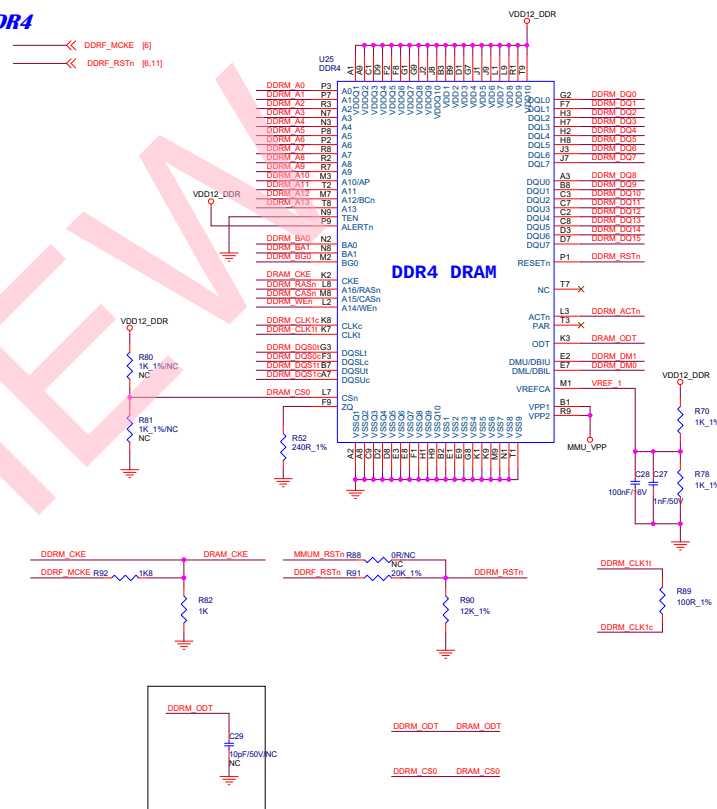
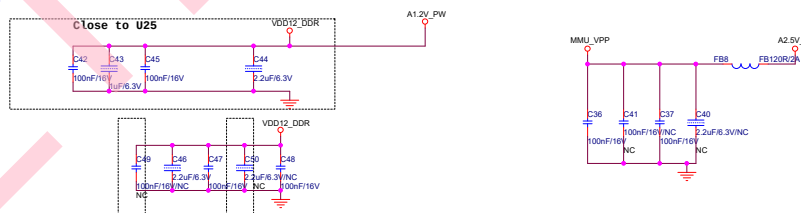
WIFI_MODULE(7663)



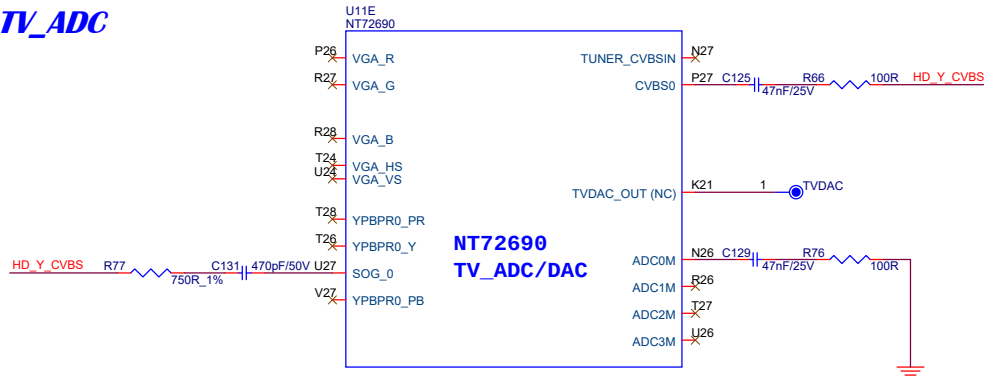
DDR_S



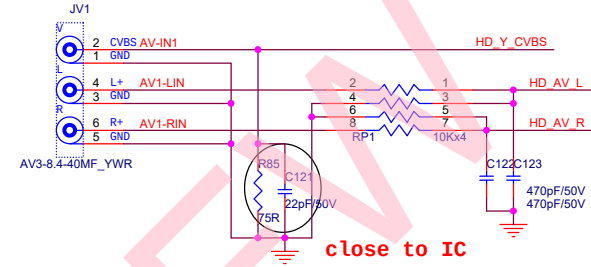
DDR_M

**DDR4****DDR4_POWER**

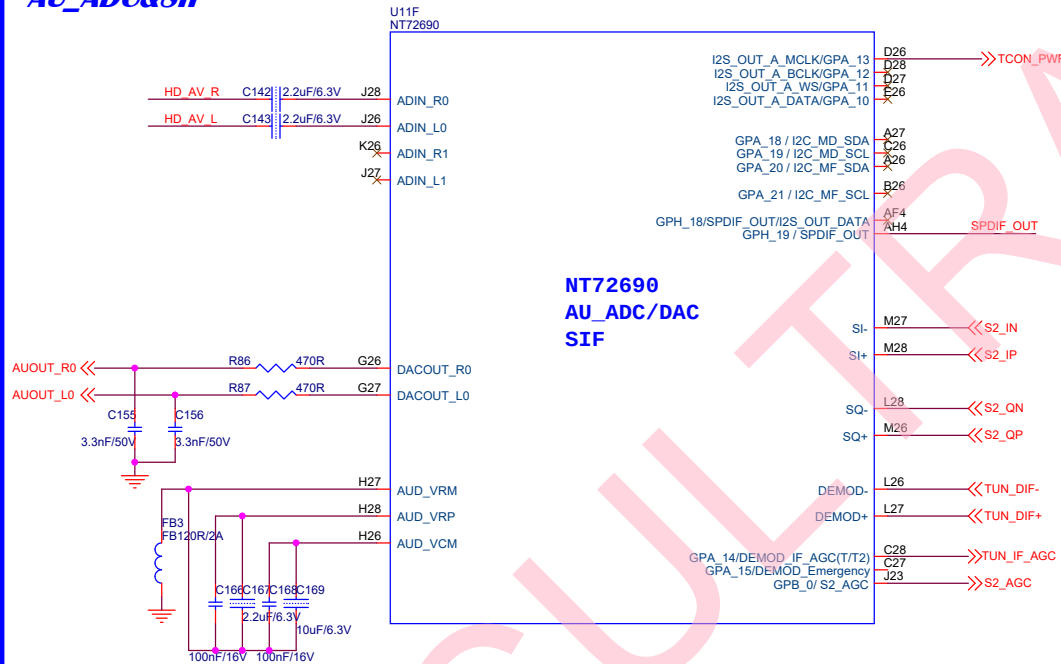
TV_ADC



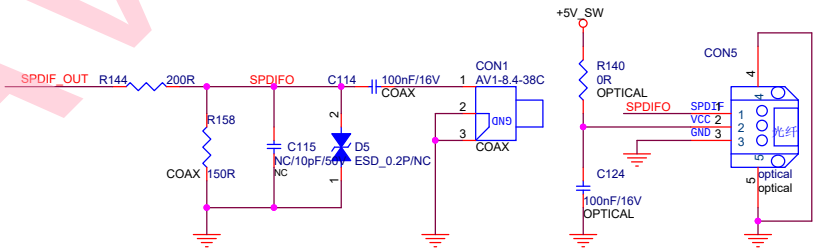
AV1_IN



AU_ADC&SIF

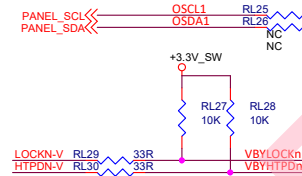
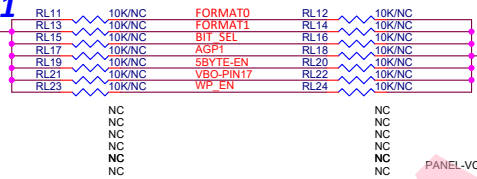


SPDIF OUT

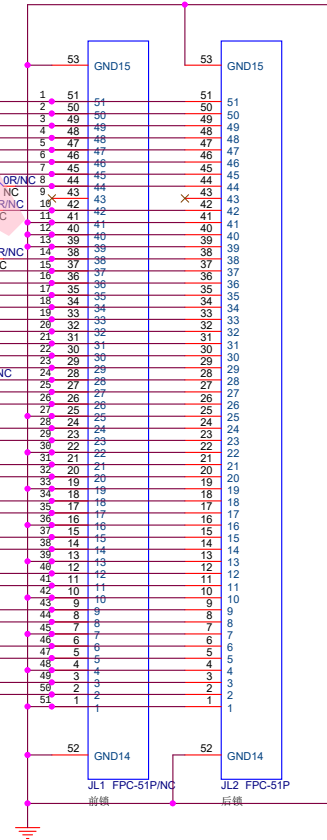
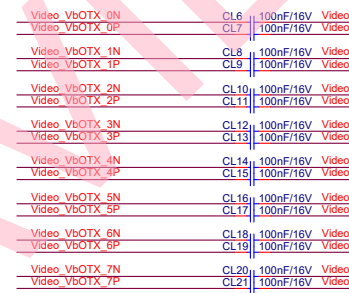


4K60Hz VB1

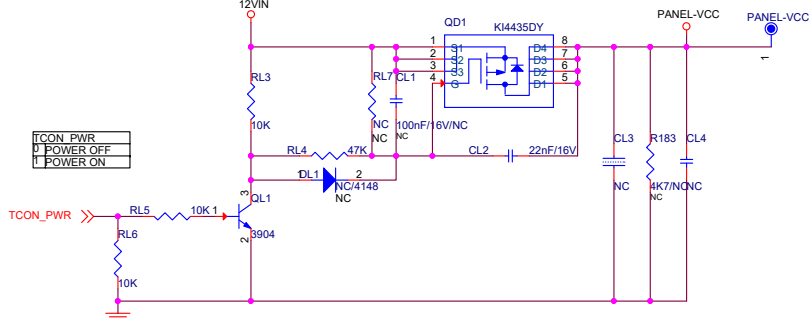
+3.3V_SW



TCON LESS 上0R电阻



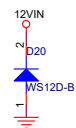
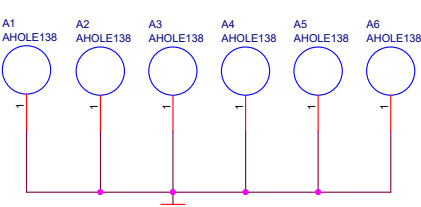
PANEL POWER



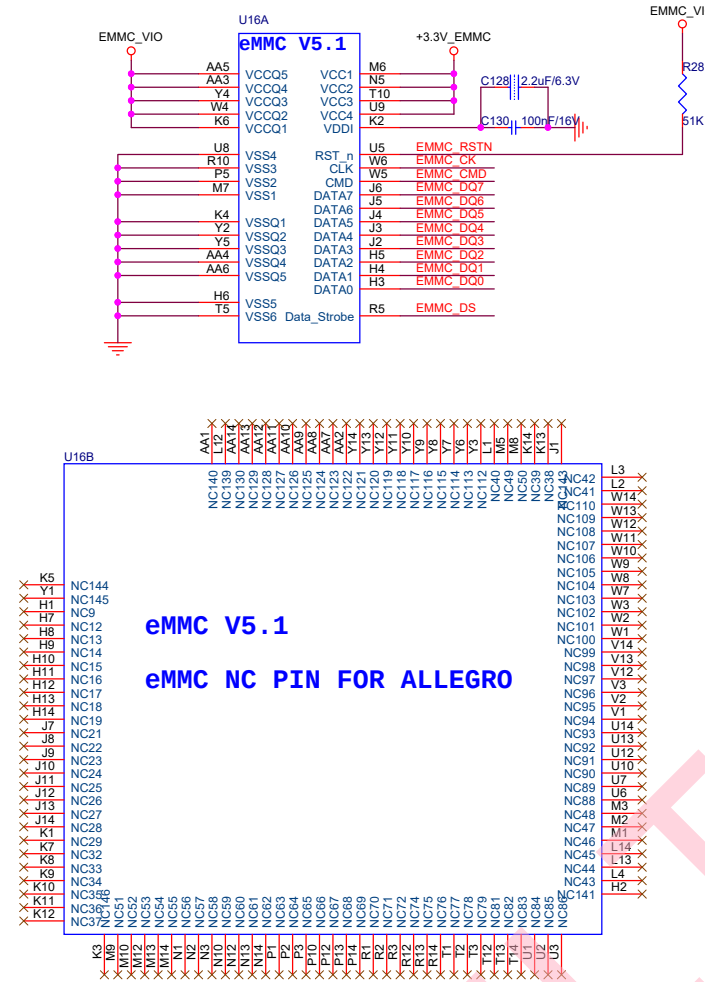
BackLight Controller



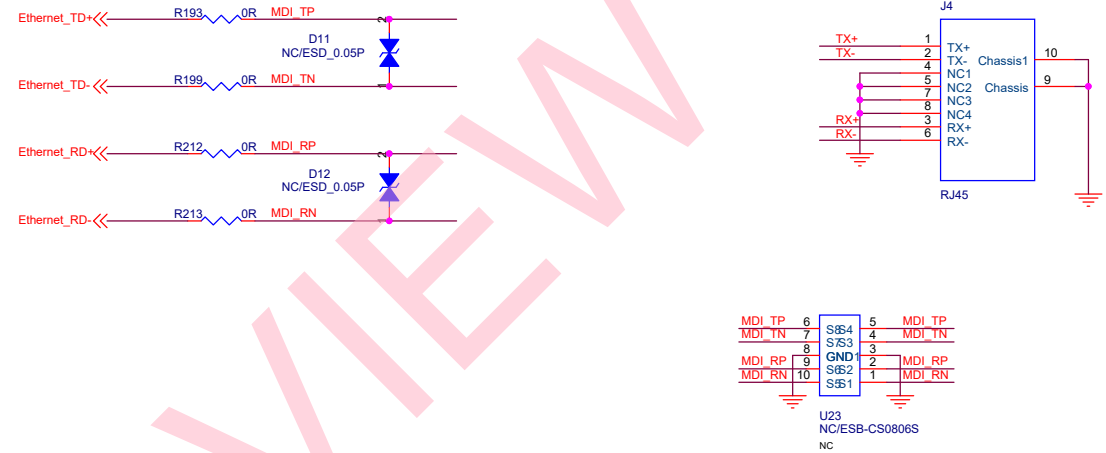
MARK & HOLE



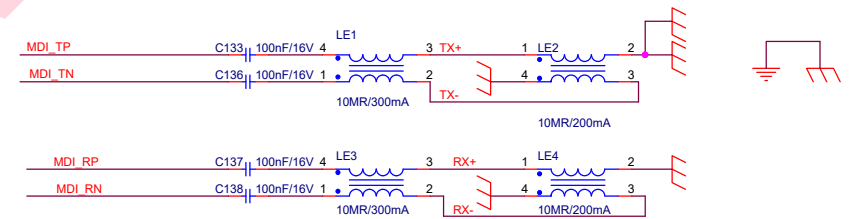
EMMC



Ethernet Connetor



ENTHERNET 分立



EMMC Power



IFAGC_T R41 10K SI_TUNER <<TUN_IF_AGC

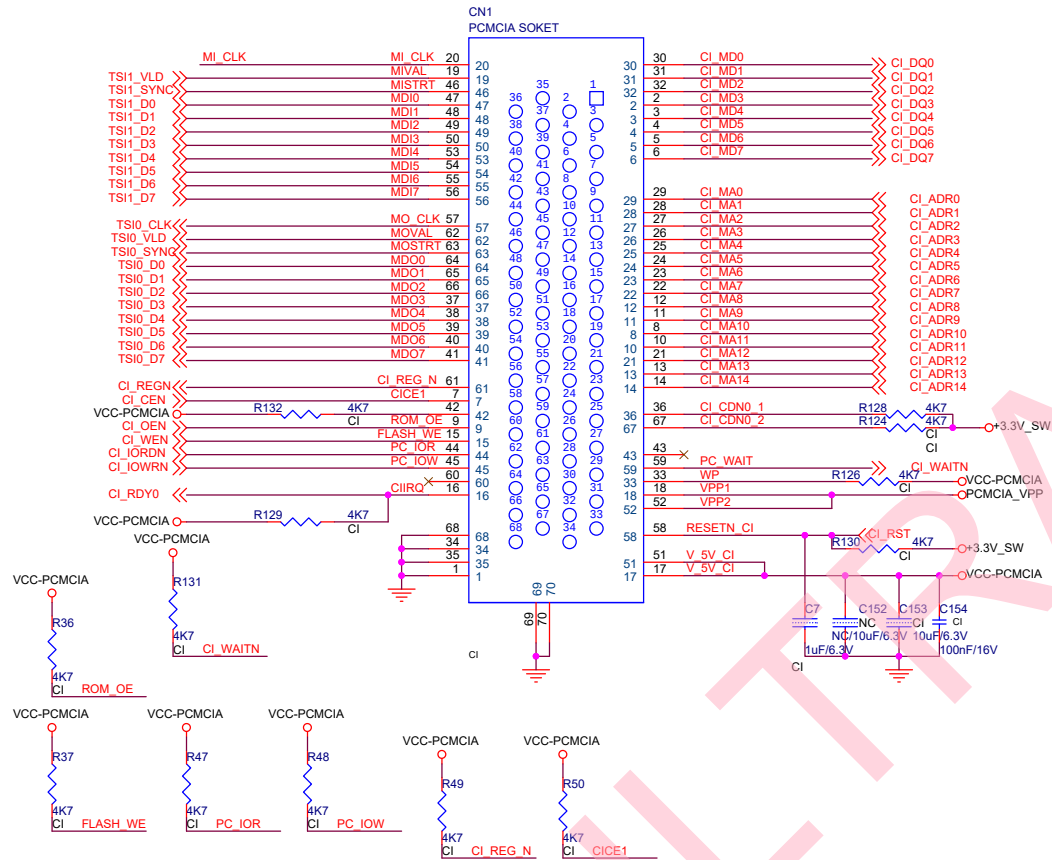
C97 100nF/16V SI_TUNER

close to SOC

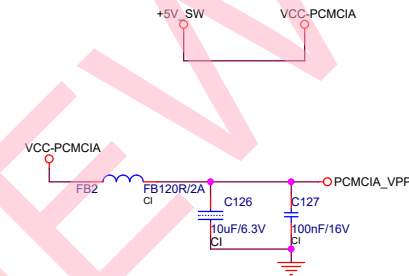
The diagram illustrates the SI_TUNER circuit. It features two input lines, DIF+ and DIF-, which are connected to resistors R39 and R40. These resistors are connected to a common node that branches into two outputs, TUN_DIF+ and TUN_DIF-. The circuit is labeled "close to SOC".

[illegible]

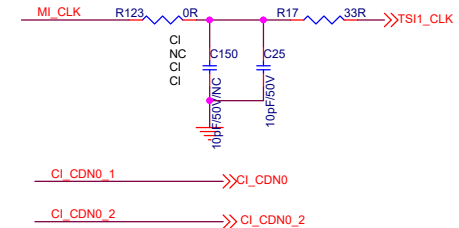
CI Socket



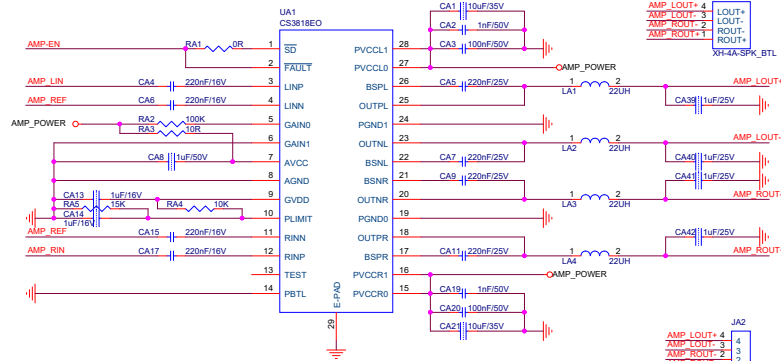
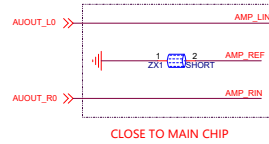
PCMCIA POWER



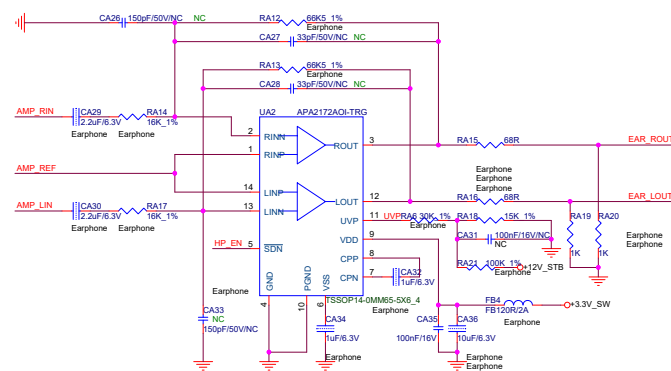
CLK



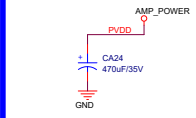
AMPLIFY



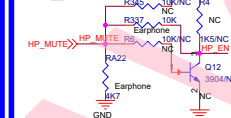
Headphone OP



POWER



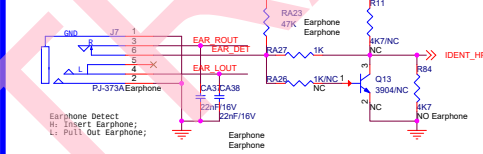
HP_MUTE



MUTE



EARPHONE



MUTE Control

